

METHOD AND SYSTEM FOR PROVIDING A PHASE-LOCKED
LOOP WITH REDUCED SPURIOUS TONES

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ABSTRACT OF THE DISCLOSURE

A method for providing a phase-locked loop with reduced spurious tones is provided that includes comparing a reference clock signal to an internal clock signal to generate a first signal. The first signal is sampled based on a sampling clock
10 signal to generate a second signal. The sampling clock signal is reduced with respect to the reference clock signal. The internal clock signal is generated based on the second signal.